



PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): David M. Fried et al.

Examiner: Ginette Peralta

Serial No: 09/683,328

Art Unit: 2814

Filed: December 14, 2001

Docket: BUR920010171US1 (15017)

For IMPLANTED ASYMMETRIC DOPED
POLYSILICON GATE FinFET

Dated: January 8, 2003

Assistant Commissioner for Patents
United States Patent and Trademark Office
Washington, D.C. 20231

RECEIVED
JAN 14 2003
TECHNOLOGY CENTER 2800

RESPONSE UNDER 37 C.F.R. §1.111 AND §1.143

Sir:

In response to the Office Action dated December 16, 2002, applicants provisionally elect, without traverse, the subject matter of Group II, i.e., Claims 12-28, for continued prosecution in the above-identified patent application.

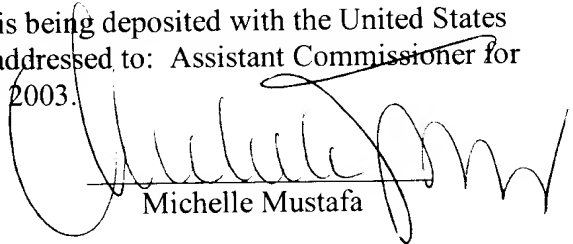
REMARKS

Favorable consideration and allowance of the claims of the present application

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on January 8, 2003.

Dated: January 8, 2003


Michelle Mustafa